Notice of Allowability	Application No.	Applicant(s)
	10/637,663	ITOH ET AL.
	Examiner	Art Unit
	Steven D. Radosevich	2117
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>1/31/07</u> .		
2. The allowed claim(s) is/are <u>1-13</u> .		
 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some* c) None of the: 1. Certified copies of the priority documents have been received. 		
2. Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) hereto or 2) to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s) 1. Notice of References Cited (PTO-892)	5. ☐ Notice of Informal P	atent Application
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary	• •
	Paper No./Mail Dat	e
 Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date <u>11/15/2006</u> 	7. Examiner's Amendn	nent/Comment
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛛 Examiner's Stateme	ent of Reasons for Allowance
or biological material	9. Other	
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Application/Control Number: 10/637,663

Art Unit: 2117

DETAILED ACTION

Claims 1-16 are present within this instant examination. Claims 14-16 are acknowledged as canceled by the applicant and will not be given further consideration within this instant examination. Applicant indicated the cancellation of claims 14-16 within the applicant's remarks filed on 01/31/2007 within the Introduction.

Priority

Acknowledgment is made that foreign priority is claimed for the application thus giving the date 02/13/2003 as the date used within this examination.

Allowable Subject Matter

The following is an examiner's statement of reasons for allowance:

Claims 1-13 are allowable.

The present invention pertains to an LSI test assembly that supplies test signals between an LSI tester and a target LSI or LSI under test. The claimed invention recites features such as: "... the assembly comprising ... a peripheral circuit coupled to the target LSI and allowing the target LSI to operate in the same manner as in actual operation; a first clock generator for generating a first clock as a reference clock for the actual operation in the target LSI; a second clock generator for generating a second clock to allow the target LSI to obtain the test signal from the LSI tester; a third clock generator for generating a third clock to allow the target LSI to output a test result signal, wherein the first clock, and second clock, and the third clock are asynchronous with each other; and a printed circuit board on which the peripheral circuit and the first, second, and third clock generators are mounted."

None of the prior arts of record, either taken by itself or in ant combination, would have anticipated or made obvious the following limitations combined with the above limitations at or before the invention was filed: "...a first clock generator for generating a first clock as a reference clock for the actual operation in the target LSI; a second clock generator for generating a second clock to allow the target LSI to obtain the test signal from the LSI tester; a third clock generator for generating a third clock to allow the target LSI to output a test result signal, wherein the first clock, and second clock, and the third clock are asynchronous with each other; and a printed circuit board on which the peripheral circuit and the first, second, and third clock generators are mounted."

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven D. Radosevich whose telephone number is 571-272-2745. The examiner can normally be reached on 9am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Cynthia H. Britt can be reached on 571-2723815. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Steven D. Radosevich

Examiner

Art Unit 2117

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